

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (original): A method comprising:

invalidating an entry of a filter coupled to a pipeline resource if an update to the entry occurs during a context; and

flushing a portion of the pipeline resource corresponding to an address space including the entry.

Claim 2 (original): The method of claim 1, further comprising flushing the portion upon a switch from the context.

Claim 3 (original): The method of claim 1, wherein the pipeline resource comprises a translation lookaside buffer.

Claim 4 (original): The method of claim 1, further comprising comparing an address obtained from an external snoop to a plurality of entries in the filter to determine if the update has occurred.

Claim 5 (original): The method of claim 1, further comprising flushing the portion of the pipeline resource via microcode.

Claim 6 (original): A method comprising:

flushing a portion of entries of a pipeline resource if one of the portion of entries is updated during a context.

Claim 7 (original): The method of claim 6, further comprising selectively flushing the portion of entries upon a switch from the context.

Claim 8 (original): The method of claim 7, wherein the portion of entries comprises an address space corresponding to the context.

Claim 9 (original): The method of claim 6, wherein the pipeline resource comprises a translation lookaside buffer.

Claim 10 (original): The method of claim 9, further comprising invalidating entries of a filter coupled to the translation lookaside buffer corresponding to the portion of entries.

Claims 11-13 (cancel)

Claims 14-16 (canceled)

Claim 17 (previously presented): An apparatus comprising:

a pipeline resource having a plurality of address spaces, each of the plurality of address spaces corresponding to one of a plurality of contexts, each one of the plurality of address spaces selectively flushable while the other address spaces are maintained.

Claim 18 (original): The apparatus of claim 17, wherein the pipeline resource comprises a translation lookaside buffer.

Claim 19 (original): The apparatus of claim 17, further comprising a filter coupled to the pipeline resource to select at least one of the plurality of address spaces to be flushed.

Claim 20 (original): The apparatus of claim 19, wherein the filter comprises a content addressable memory.

Claim 21 (original): A method comprising:

dynamically partitioning a filter of a pipeline resource into a plurality of partitions, each of the partitions corresponding to one of a plurality of address spaces.

Claim 22 (original): The method of claim 21, further comprising sharing the pipeline resource among a plurality of applications, each corresponding to one of the plurality of address spaces.

Claim 23 (original): The method of claim 22, wherein each of the plurality of partitions includes a fixed portion and wherein the filter further comprises a dynamic portion.

Claim 24 (original): The method of claim 23, further comprising allocating at least part of the dynamic portion to one of the plurality of applications that has consumed the fixed portion of one of the plurality of partitions.

Claim 25 (original): An article comprising a machine-readable storage medium containing instructions that if executed enable a system to:

dynamically partition a filter of a pipeline resource into a plurality of partitions, each of the partitions corresponding to one of a plurality of address spaces.

Claim 26 (original): The article of claim 25, further comprising instructions that if executed enable the system to permit a plurality of applications, each corresponding to one of the plurality of address spaces, to share the pipeline resource.

Claim 27 (original): The article of claim 26, further comprising instructions that if executed enable the system to allocate at least part of a dynamic portion of the filter to one of the plurality of applications that has consumed one of the plurality of partitions.

Claim 28 (previously presented): A system comprising:

a first processor having a pipeline resource having a plurality of address spaces, each of the plurality of address spaces corresponding to one of a plurality of contexts, each one of the plurality of address spaces selectively flushable while the other address spaces are maintained; and

a dynamic random access memory coupled to the first processor.

Claim 29 (original): The system of claim 28, further comprising a second processor coupled to the first processor.

Claim 30 (original): The system of claim 29, further comprising a filter coupled to the pipeline resource to snoop address information from the second processor.